TCG 01-G QUICK START GUIDE

QUICK SETUP

- 1. Install the TCG 01-G in the desired location
- 2. Connect the TCG 01-G to the network switch
- Connect to power, ensuring correct voltage is used
- 4. When running the TCG 01-G for the first time, Time, Output, Network, and Security settings can be configured by running the Configuration Tool software on any PC on the same network as the TCG 01-G. See Configuration Tool Tips and Networking Tips for more help
- 5. Once the TCG 01-G is fully setup, connect the clock outputs to your device/s

CONFIGURATION TOOL TIPS

- The Configuration Tool software is available from www.tekron.com
- To connect to your TCG 01-G: press the Discover button, then select the TCG 01-G from the list, then press the Configure button Default login is – User Name: admin, Password: Password*
- Upon first connection you will be prompted to change the default password
- In most cases, set the "Cable Delay" to 4ns for every meter of antenna cable, and the "Mask Angle" to 5 degrees. This can be set on the GPS tab
- In most applications, the TTL output should be configured to IRIG-B, with "Extensions" set to C37.118 (previously IEEE 1344, see right)
- Set this by going to the I/O tab, then for each port:
 - 1) Select the port
 - 2) Set the output type to IRIG-B
 - 3) Change the extensions to C37.118

*Warning: Please ensure that you record your password and store it in a secure manner. In accordance with Cyber Security "best practice", if the administrative passwords are lost, the device must be sent back to the manufacture to recover the password



NETWORKING TIPS

- If the TCG 01-G does not appear when you press Discover in the Configuration Tool, connect the TCG 01-G directly using an Ethernet cable.
- Ensure your firewall(s) has UDP exceptions for the configuration program and for ports 9992, 9997 & 9999
- Ensure you have administrative rights on your PC

DEVICE CONNECTION TIPS

- It's recommended a twisted pair cable is used to connect devices to the TTL port
- A termination 120 Ωresistor can be added to the end of a TTL run to achieve good impedance match



Rear Panel – Inputs & Outputs



